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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/500,620	07/01/2004	Jigang Liu	CN 020002	4330
24737	7590	01/11/2006	EXAMINER	
PHILIPS INTELLECTUAL PROPERTY & STANDARDS P.O. BOX 3001 BRIARCLIFF MANOR, NY 10510			NGUYEN, TUAN HOANG	
			ART UNIT	PAPER NUMBER
			2643	

DATE MAILED: 01/11/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/500,620

Applicant(s)

LIU, JIGANG

Examiner

Tuan H. Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 01 July 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>05/09/2005</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on 05/09/2005 has been considered by Examiner and made of record in the application file.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-14 are rejected under 35 U.S.C. 102(b) as being anticipated by Westergren et al. (US PAT. 6,115,409 hereinafter, "Westergren").

Regarding claim 1, Westergren discloses transceiver for transmitting signals in a transmitting mode and for receiving signals in a receiving mode (col. 4 lines 12-15) and comprising a digital synthesizer (item 58) driven phase locked loop (items 38 and 57), characterized in that said digital synthesizer driven phase locked loop (items 38 and 57), in said transmitting mode, is in a modulating state (col. 8 lines 18-21), with said digital synthesizer driven phase locked loop (items 38 and 57), in said receiving mode, being in an oscillating state (Fig. 1 and Fig. 3, col. 4 lines 12-66 and col. 10 lines 38-43).

Regarding claim 2, Westergren further discloses characterized in that said digital synthesizer (item 58) driven phase locked loop (items 57) receives, in said modulating state, a modulation signal (col. 10 line 38 through col. 11 line 2), with said digital synthesizer driven phase locked loop (items 38 and 57), in said oscillating state, receiving a non-modulation signal (Fig. 1 and Fig. 3 col. 2 lines 34-45).

Regarding claim 3, Westergren further discloses characterized in that said transceiver (item 10) comprises a controller (item 59) for generating said modulation signal and for generating control signals, with a switch (item 139) being coupled to said controller and said digital synthesizer driven phase locked loop (items 38 and 57) for in response to a first control signal supplying said modulation signal from said controller to said digital synthesizer driven phase locked loop (items 38 and 57) and in response to a second control signal supplying said non-modulation signal to said digital synthesizer

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driven phase locked loop (items 38 and 57 col. 6 lines 35-50).

Regarding claim 6, Westergren further discloses characterized in that said digital synthesizer driven phase locked loop (items 38 and 57), in said modulating state, generates a modulated signal (col. 10 line 38 through col. 11 line 2), with said digital synthesizer driven phase locked loop (items 38 and 57), in said oscillating state, generating a non-modulated signal (col. 2 lines 34-45).

Regarding claim 7, Westergren further discloses characterized in that an output of said digital synthesizer driven phase locked loop (items 57) is coupled via a first switch (item 132) and a transmitter part and a second switch (item 139) to an antenna (item 14) for in response to a first control signal supplying said modulated signal to said antenna for transmitting said modulated signal, with said first switch further being coupled to a first input of a demodulator and with said second switch further being coupled via a receiver part to a second input of said demodulator for in response to a second control signal supplying said non-modulated signal to said demodulator for demodulating a radio signal received via said antenna (Fig. 1 and Fig. 3 col. 9 lines 6-34).

Regarding claim 8, Westergren further discloses digital synthesizer (item 58) driven phase locked loop (items 38 and 57) for use in a transceiver for transmitting signals in a transmitting mode and for receiving signals in a receiving mode (col. 4 lines 12-15) and comprising said digital synthesizer driven phase locked loop, characterized in that said digital synthesizer driven phase locked loop, in said transmitting mode, is in a modulating state (col. 4 lines 24-30), with said digital synthesizer driven phase locked loop, in said receiving mode, being in an oscillating state (col. 10 lines 38-43 and Fig. 1 and Fig. 3 col. 4 lines 12-66).

Regarding claim 9, Westergren further discloses phase locked loop (items 38 and 57) for use in a digital synthesizer driven phase locked loop for use in a transceiver for transmitting signals in a transmitting mode and for receiving signals in a receiving mode (col. 4 lines 12-15), and comprising said digital synthesizer driven phase locked loop, characterized in that said phase locked loop, in said transmitting mode, is in a modulating state (col. 4 lines 24-30), with said phase locked loop, in said receiving mode, being in an oscillating state (col. 10 lines 38-43 and Fig. 1 and Fig. 3 col. 4 lines 12-66).

Regarding claim 10, Westergren further discloses digital synthesizer (item 58) for use in a digital synthesizer driven phase locked loop (items 38 and 57) for use in a transceiver for transmitting signals in a transmitting mode and for receiving signals in a receiving mode (col. 4 lines 12-15), and comprising said digital synthesizer driven phase locked loop, characterized in that said digital synthesizer, in said transmitting mode, is in a modulating state (col. 4 lines 24-30), with said digital synthesizer, in said receiving mode, being in an oscillating state (col. 10 lines 38-43 and Fig. 1 and Fig. 3 col. 4 lines 12-66).

Regarding claim 11, Westergren further discloses system comprising at least one portable unit and at least one network unit for radio communication, with at least one unit comprising at least one transceiver for transmitting signals in a transmitting mode and for receiving signals in a receiving mode (col. 4 lines 12-15), and comprising a digital synthesizer driven phase locked loop (items 38 and 57), characterized in that said digital synthesizer driven phase locked loop, in said transmitting mode, is in a modulating state (col. 4 lines 24-30), with said digital synthesizer driven phase locked loop, in said receiving mode, being in an oscillating state (col. 10 lines 38-43 and Fig. 1 and Fig. 3 col. 4 lines 12-66).

Regarding claim 12, Westergren further discloses portable unit comprising a transceiver for transmitting signals in a transmitting mode and for receiving signals in a receiving mode (col. 4 lines 12-15), and comprising a digital synthesizer driven phase locked loop (items 38 and 57), characterized in that said digital synthesizer driven phase locked loop, in said transmitting mode, is in a modulating state (col. 4 lines 24-30), with said digital synthesizer driven phase locked loop, in said receiving mode, being in an oscillating state (col. 10 lines 38-43 and Fig. 1 and Fig. 3 col. 4 lines 12-66).

Regarding claim 13, Westergren further discloses network unit comprising at least one transceiver for transmitting signals in a transmitting mode and for receiving signals in a receiving mode (col. 4 lines 12-15), and comprising a digital synthesizer driven phase locked loop (items 38 and 57), characterized in that said digital synthesizer driven phase locked loop, in said transmitting mode, is in a modulating state (col. 4 lines 24-30), with said digital synthesizer driven phase locked loop, in said receiving mode, being in an oscillating state (col. 10 lines 38-43 and Fig. 1 and Fig. 3 col. 4 lines 12-66).

Regarding claim 14, Westergren further discloses method for transmitting signals in a transmitting mode and for receiving signals in a receiving mode via a digital synthesizer driven phase locked loop (items 38 and 57), characterized in that said method comprises a first step of bringing said digital synthesizer driven phase locked loop, in said transmitting mode, in a modulating state (col. 4 lines 24-30), and a second step of bringing said digital synthesizer driven phase locked loop, in said receiving mode, in an oscillating state (col. 10 lines 38-43 and Fig. 1 and Fig. 3 col. 4 lines 12-66).

Allowable Subject Matter

5. Claims 4-5 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

6. Any response to this action should be mailed to:

Mail Stop_____ (Explanation, e.g., Amendment or After-final, etc.)

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Facsimile responses should be faxed to:

(703) 872-9306

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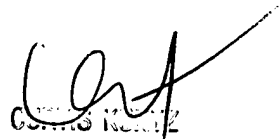
Alexandria, VA 22313

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan H. Nguyen whose telephone number is (571)272-8329. The examiner can normally be reached on 8:00Am - 5:00Pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Curtis Kuntz can be reached on (571)272-7499. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Tuan Nguyen
Examiner
Art Unit 2643


CURTIS KUNTZ
SUPERVISOR, PATENT EXAMINER
ELECTRONIC BUSINESS CENTER 8300